

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In Re the Application of:

MAURICIO J. SERRANO, ET AL.

Application No.: 10/725730

Filed: December 1, 2003

For: **MEMORY TRACE BUFFER**

Art Group: 2186

Examiner: Dudek, Jr., Edward J.

**INFORMATION DISCLOSURE STATEMENT UNDER 37 C.F.R. §1.97**

Commissioner for Patents  
P.O. Box 1450  
Alexandria, VA 22313-1450

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Respectfully submitted,

BLAKELY, SOKOLOFF, TAYLOR & ZAFMAN LLP

Date: March 19, 2007

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**INFORMATION DISCLOSURE  
STATEMENT BY APPLICANT**  
*(Use as many sheets as necessary)*

Complete if Known

**Application Number**

10/725,730

**Filing Date**

December 1, 2003

**First Named Inventor**

Adl-Tabatabai, Ali-Reza

**Group Art Unit**

2186

**Examiner Name**

DUDEK, JR., Edward J.

**Confirmation Number**

3094

Sheet 1 of 1

Attorney Docket No: 42P17023

**US PATENT DOCUMENTS**

Examiner Initial *	USP Document Number	Publication Date	Name of Patentee or Applicant of cited Document	Class	Subclass	Filing Date If Appropriate
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**FOREIGN PATENT DOCUMENTS**

Examiner Initials*	Foreign Document No	Publication Date	Name of Patentee or Applicant of cited Document	Class	Subclass	T <sup>2</sup>
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**OTHER DOCUMENTS -- NON PATENT LITERATURE DOCUMENTS**

Examiner Initials*	Cite No <sup>1</sup>	Include name of the author (in CAPITAL LETTERS), title of the article (when appropriate), title of the item (book, magazine, journal, serial, symposium, catalog, etc.), date, page(s), volume-issue number(s), publisher, city and/or country where published.	T <sup>2</sup>
		CALDER, BRAD , et al., "Cache-Conscious Data Placement", <u>Proceedings of Conference for Architecture Support for Operating Systems, ASPLOS,(1998)</u> ,pages 139-149	
		CHILIMBI, TRISHUL M., et al., "Cache-Conscious Structure Layout", <u>Proceedings of PLDI, (1999)</u> ,12 pages	
		CHILIMBI, TRISHUL M., et al., "Using Generational Garbage Collection To Implement Cache-Conscious Data Placement", <u>Proceedings of International Symposium on Memory Management, ISMM'98,</u> (1998),12 pages	
		LIAO, STEVEN S., et al., "Post-Pass Binary Adaptation for Software-Based Speculative Precomputation", <u>Proceedings of PLDI, (2002)</u> ,12 pages	
		WU, YONGFENG , et al., "Value-Profile Guided Stride Prefetching for Irregular Code", <u>Proceedings of the Compiler Construction Conference (CC2002), Grenoble, France, (2002)</u> ,pages 307-324	

**EXAMINER****DATE CONSIDERED**

Substitute Disclosure Statement Form (PTO-1449)

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